

IN THE SPECIFICATION

p. 6, ln. 20: Before "Fig. 4" insert --

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Fig. 3A illustrates the programmable gray-scale LCD of the present invention in a typical configuration including a polarizer and an analyzer.

p. 9, ln. 23: After "." Insert --Interface circuitry 46 and electronically programmable driver circuitry 48 may be formed on SOS wafers 42 according to well known techniques to provide gray-scale control 50. Programmable gray-scale LCD 40 may be applied to the typical configuration of Fig. 3 with polarizer 16 and analyzer 17 as shown in Fig. 3A.--

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REMARKS

Status of Claims

Claims 1-11 are pending in the application.

Claims 1-3, 6, 10-11 were rejected under 35 USC 102(b) as being anticipated by the prior art.

Claims 4-5, 8, 9 were rejected under 35 USC 103(a) as being unpatentable over the prior art in view of Johary et al.

Claim 7 was rejected under 35 USC 103(a) as being unpatentable over the prior art in view of Kobayashi et al.